RESISTIVE CIRCUITS

• MULTI NODE/LOOP CIRCUIT ANALYSIS

DEFINING THE REFERENCE NODE IS VITAL



$$V_{12} = __?$$



CURRENTS COULD BE COMPUTED DIRECTLY USING KCL AND CURRENT DIVIDER!!

Hint: Each voltage source connected to the reference node saves one node equation

3 nodes plus the reference. In principle one needs 3 equations...

...but two nodes are connected to the reference through voltage sources. Hence those node voltages are known!!!

...Only one KCL is necessary $\frac{V_2}{V_2} + \frac{V_2 - V_3}{V_2 - V_1} + \frac{V_2 - V_1}{V_2 - V_1} = 0$ 6k 12k 12k $V_1 = 12[V]$ THESE ARE THE REMAINING TWO NODE EQUATIONS $V_3 = -6[V]$ SOLVING THE EQUATIONS $2V_2 + (V_2 - V_3) + (V_2 - V_1) = 0$ $4V_2 = 6[V] \Longrightarrow V_2 = 1.5[V]$

ALGEBRAIC DETAILS

The Equations (1) $\frac{V_1}{6k} + \frac{V_2}{12k} - 6mA + 4mA = 0 * 15$ (2) $V_1 - V_2 = 6[V]$

Solution

1. Eliminate denominators in Eq(1). Multiply by ...

 $2V_1 + V_2 = 24[V]$ $V_1 - V_2 = 6[V]$

2. Add equations to eliminate V_2 $3V_1 = 30[V] \Rightarrow V_1 = 10[V]$

3. Use Eq(2) to compute V_2 $V_2 = V_1 - 6[V] = 4[V]$

Apply node analysis to this circuit

There are 4 non reference nodes

There is one super node

There is one node connected to the reference through a voltage source

We need three equations to compute all node voltages

...BUT THERE IS ONLY ONE CURRENT FLOWING THROUGH ALL COMPONENTS AND IF THAT CURRENT IS DETERMINED ALL VOLTAGES CAN BE COMPUTED WITH OHM'S LAW

DETERMINATION OF LOOP CURRENTS KVL ON LEFT MESH $v_1 + v_3 + v_2 - v_{s1} = 0$ KVL ON RIGHT MESH $v_{32} + v_4 + v_5 - v_3 = 0$ USING OHM'S LAW $v_1 = i_1 R_1, v_2 = i_1 R_2, v_3 = (i_1 - i_2) R_3$ $v_{4} = i_{2}R_{4}, v_{5} = i_{2}R_{5}$ REPLACING AND REARRANGING $i_1(R_1 + R_2 + R_3) - i_2(R_3) = v_{S1}$ $-i_1(R_3) + i_2(R_3 + R_4 + R_5) = -v_{s2}$

DEVELOPING A SHORTCUT

WRITE THE MESH EQUATIONS

WHENEVER AN ELEMENT HAS MORE THAN ONE LOOP CURRENT FLOWING THROUGH IT WE COMPUTE NET CURRENT IN THE DIRECTION OF TRAVEL

DRAW THE MESH CURRENTS. ORIENTATION CAN BE ARBITRARY. BUT BY CONVENTION THEY ARE DEFINED CLOCKWISE

NOW WRITE KVL FOR EACH MESH AND APPLY OHM'S LAW TO EVERY RESISTOR.

AT EACH LOOP FOLLOW THE PASSIVE SIGN CONVENTION USING LOOP CURRENT REFERENCE DIRECTION

 $-V_1 + I_1 R_1 + (I_1 - I_2) R_2 + I_1 R_5 = 0$ $V_2 + I_2 R_3 + I_2 R_4 + (I_2 - I_1) R_2 = 0$

3. SOLVE EQUATIONS

$$8I_{1} - 2I_{2} = -3[mA]$$

-2I_{1} + 8I_{2} = 9[mA] */4 and add
$$30I_{2} = 33[mA]$$

$$V_{0} = 6kI_{2} = \frac{33}{5}[V]$$

CURRENT SOURCES SHARED BY LOOPS - THE SUPERMESH APPROACH

2. WRITE CONSTRAINT EQUATION DUE TO MESH CURRENTS SHARING CURRENT SOURCES $I_2 - I_3 = 4mA$

3. WRITE EQUATIONS FOR THE OTHER MESHES

$$\boldsymbol{I}_1 = 2\boldsymbol{m}\boldsymbol{A}$$

4. DEFINE A <u>SUPERMESH</u> BY (MENTALLY) REMOVING THE SHARED CURRENT SOURCE

5. WRITE KVL FOR THE SUPERMESH

$$-6 + 1kI_3 + 2kI_2 + 2k(I_2 - I_1) + 1k(I_3 - I_1) = 0$$

NOW WE HAVE THREE EQUATIONS IN THREE UNKNOWNS. THE MODEL IS COMPLETE

For loop analysis we notice...

Three independent current sources. Four meshes.

One current source shared by two meshes.

Careful choice of loop currents should make only one loop equation necessary. Three loop currents can be chosen using meshes and not sharing any source. Now we need a loop current that does not go over any current source and passes through all unused components.

HINT: IF ALL CURRENT SOURCES ARE REMOVED THERE IS ONLY ONE LOOP LEFT

> MESH EQUATIONS FOR LOOPS WITH CURRENT SOURCES

> > $I_1 = I_{s1}$ $I_2 = I_{s2}$ $I_3 = I_{s3}$

KVL OF REMAINING LOOP

$$V_{S} + R_{3}(I_{4} - I_{2}) + R_{1}(I_{4} + I_{3} - I_{1}) + R_{4}(I_{4} + I_{3}) = 0$$

SOLVE FOR THE CURRENT 14. USE OHM'S LAW TO COMPUTE REQUIRED VOLTAGES

$$V_1 = \boldsymbol{R}_1(\boldsymbol{I}_1 - \boldsymbol{I}_3 - \boldsymbol{I}_4)$$
$$V_2 = \boldsymbol{R}_2(\boldsymbol{I}_2 - \boldsymbol{I}_1)$$
$$V_3 = \boldsymbol{R}_3(\boldsymbol{I}_2 - \boldsymbol{I}_4)$$