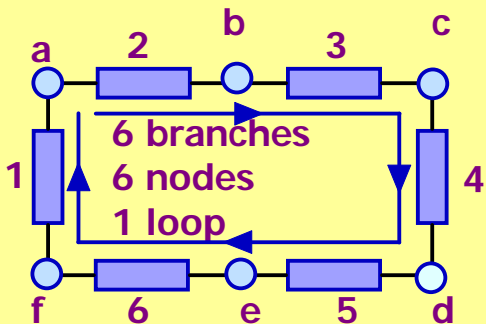


RESISTIVE CIRCUITS

- SINGLE LOOP CIRCUIT ANALYSIS

SINGLE LOOP CIRCUITS

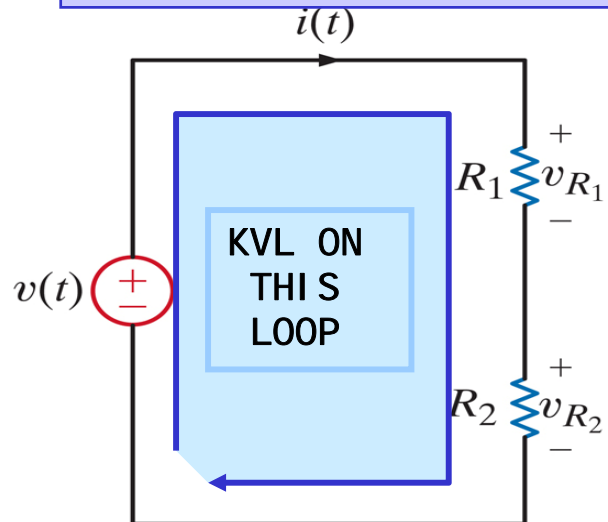
BACKGROUND: USING KVL AND KCL WE CAN WRITE ENOUGH EQUATIONS TO ANALYZE ANY LINEAR CIRCUIT. WE NOW START THE STUDY OF SYSTEMATIC, AND EFFICIENT, WAYS OF USING THE FUNDAMENTAL CIRCUIT LAWS



ALL ELEMENTS IN SERIES
ONLY ONE CURRENT

WRITE 5 KCL EQS OR DETERMINE THE ONLY CURRENT FLOWING

VOLTAGE DIVISION: THE SIMPLEST CASE



$$\text{KVL} \quad -v(t) + v_{R_1} + v_{R_2} = 0$$

OHM'S LAW

$$v_{R_1} = R_1 i(t)$$

$$v_{R_2} = R_2 i(t)$$

$$\text{SUBSTITUTION} \quad v(t) = R_1 i(t) + R_2 i(t)$$

$$i(t) = \frac{v(t)}{R_1 + R_2}$$

$$v_{R_1} = R_1 i(t)$$

$$= R_1 \left[\frac{v(t)}{R_1 + R_2} \right]$$

$$v_{R_2} = \frac{R_2}{R_1 + R_2} v(t)$$

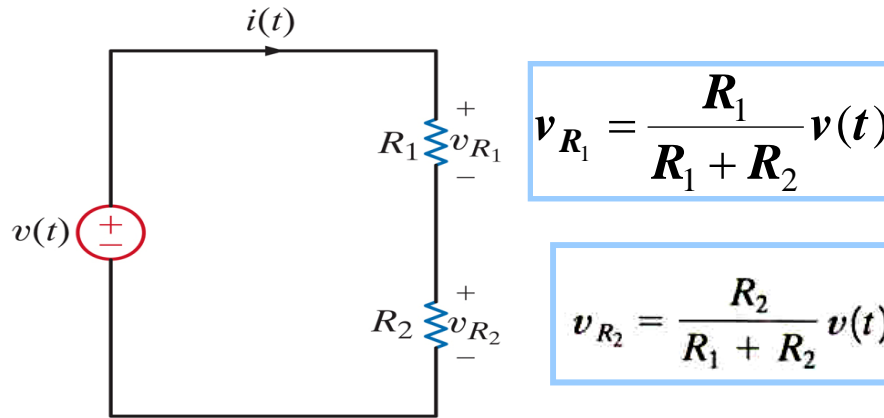
IMPORTANT VOLTAGE DIVIDER EQUATIONS

$$= \frac{R_1}{R_1 + R_2} v(t)$$

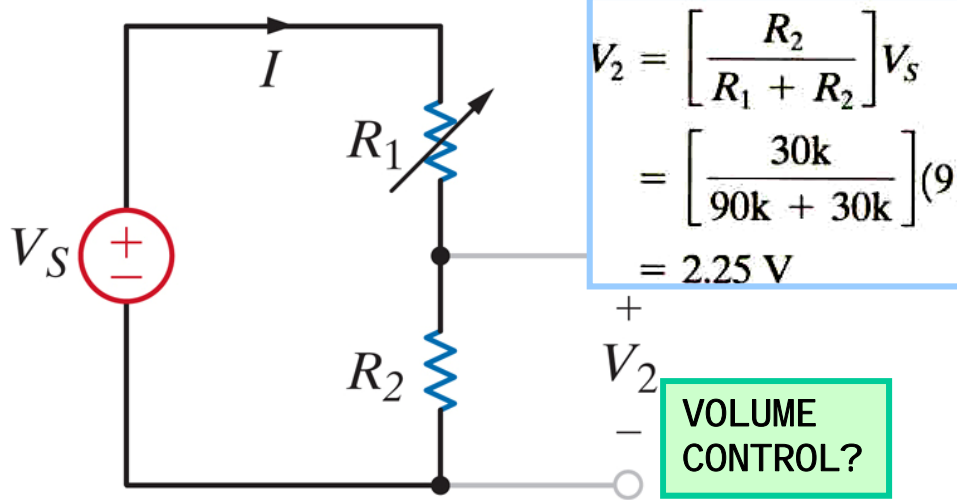
THE PLAN

- BEGIN WITH THE SIMPLEST ONE LOOP CIRCUIT
- EXTEND RESULTS TO MULTIPLE SOURCE
- AND MULTIPLE RESISTORS CIRCUITS

SUMMARY OF BASIC VOLTAGE DIVIDER



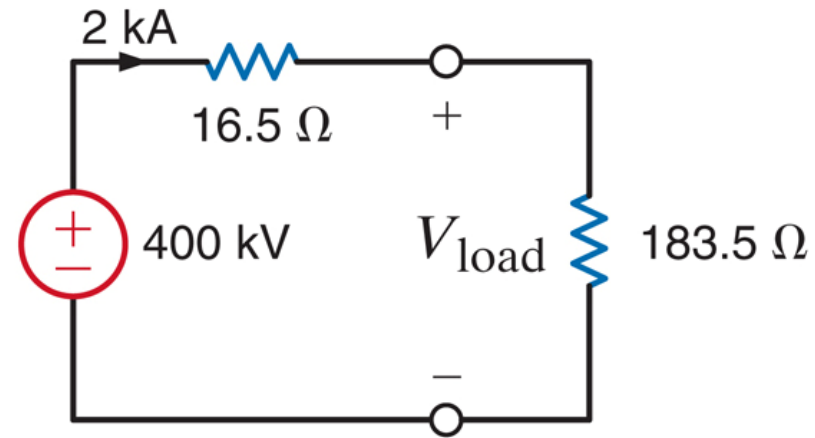
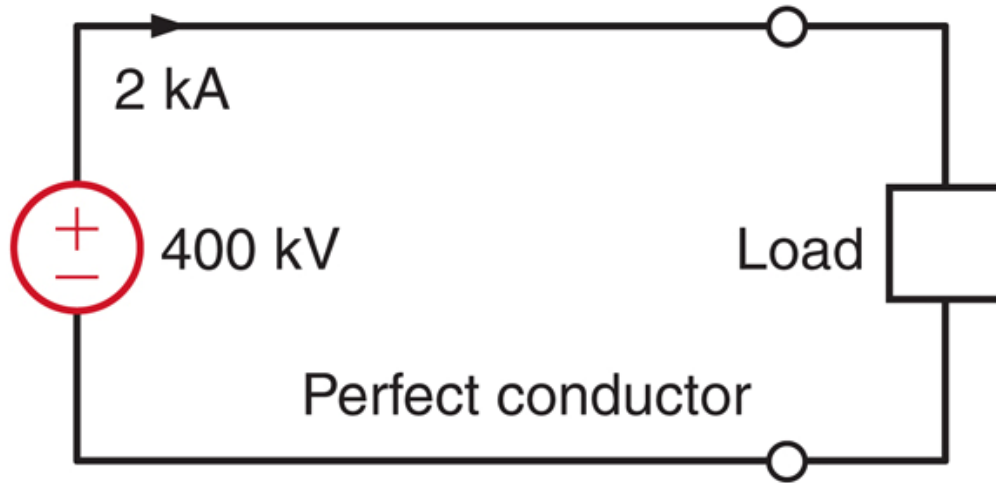
EXAMPLE : $V_S = 9V$, $R_1 = 90k\Omega$, $R_2 = 30k\Omega$



$R_1 = 15k\Omega \Rightarrow V_2 = \left[\frac{30k}{30k + 15k} \right] 9$
 $= 6 \text{ V}$

A "PRACTICAL" POWER APPLICATION

Line resistance is $0.04125 \Omega/\text{mile}$



$$V_{\text{load}} = \left[\frac{183.5}{183.5 + 16.92} \right] 400\text{k}$$

$$= 366.24 \text{ kV}$$

$$P_{\text{load}} = I^2 R_{\text{load}}$$

$$= 734 \text{ MW}$$

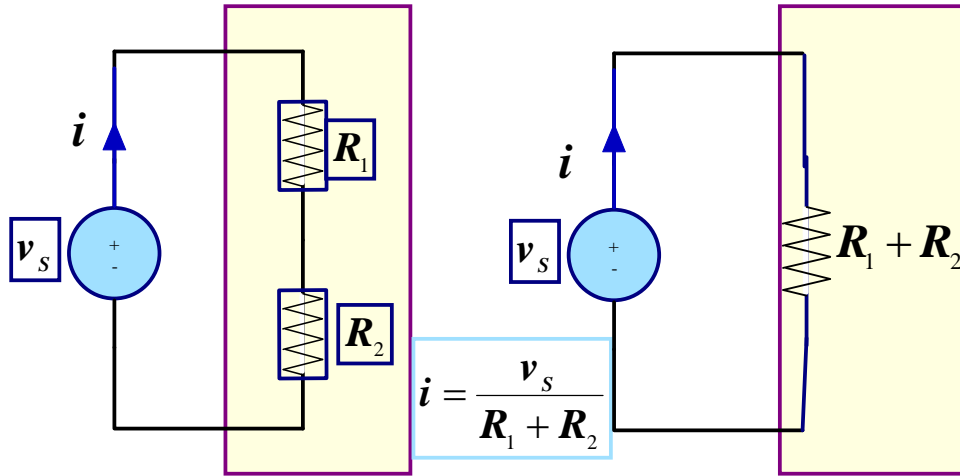
$$P_{\text{line}} = P_{\text{in}} - P_{\text{load}} = I^2 R_{\text{line}}$$

$$= 66 \text{ MW} \quad \text{LOSSES!!!}$$

HOW CAN ONE REDUCE THE LOSSES?

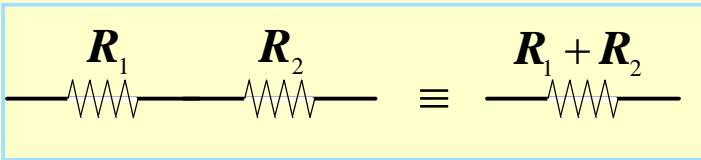
THE CONCEPT OF EQUIVALENT CIRCUIT

THIS CONCEPT WILL OFTEN BE USED TO SIMPLIFY THE ANALYSIS OF CIRCUITS. WE INTRODUCE IT HERE WITH A VERY SIMPLE VOLTAGE DIVIDER



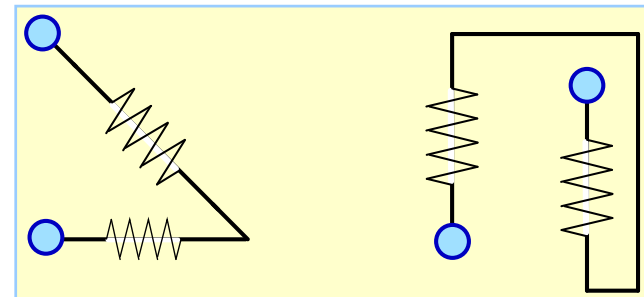
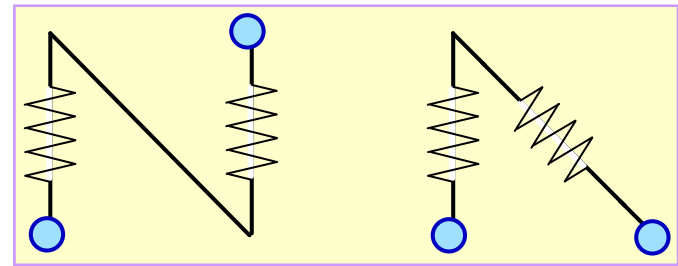
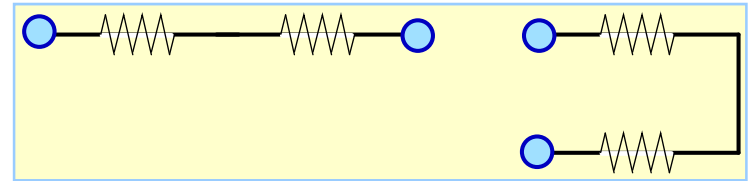
AS FAR AS THE CURRENT IS CONCERNED BOTH CIRCUITS ARE EQUIVALENT. THE ONE ON THE RIGHT HAS ONLY ONE RESISTOR

SERIES COMBINATION OF RESISTORS



THE DIFFERENCE BETWEEN ELECTRIC CONNECTION AND PHYSICAL LAYOUT

SOMETIMES, FOR PRACTICAL CONSTRUCTION REASONS, COMPONENTS THAT ARE ELECTRICALLY CONNECTED MAY BE PHYSICALLY QUITE APART



IN ALL CASES THE RESISTORS ARE CONNECTED IN SERIES

CONNECTOR SIDE

ILLUSTRATING THE DIFFERENCE BETWEEN PHYSICAL LAYOUT AND ELECTRICAL CONNECTIONS

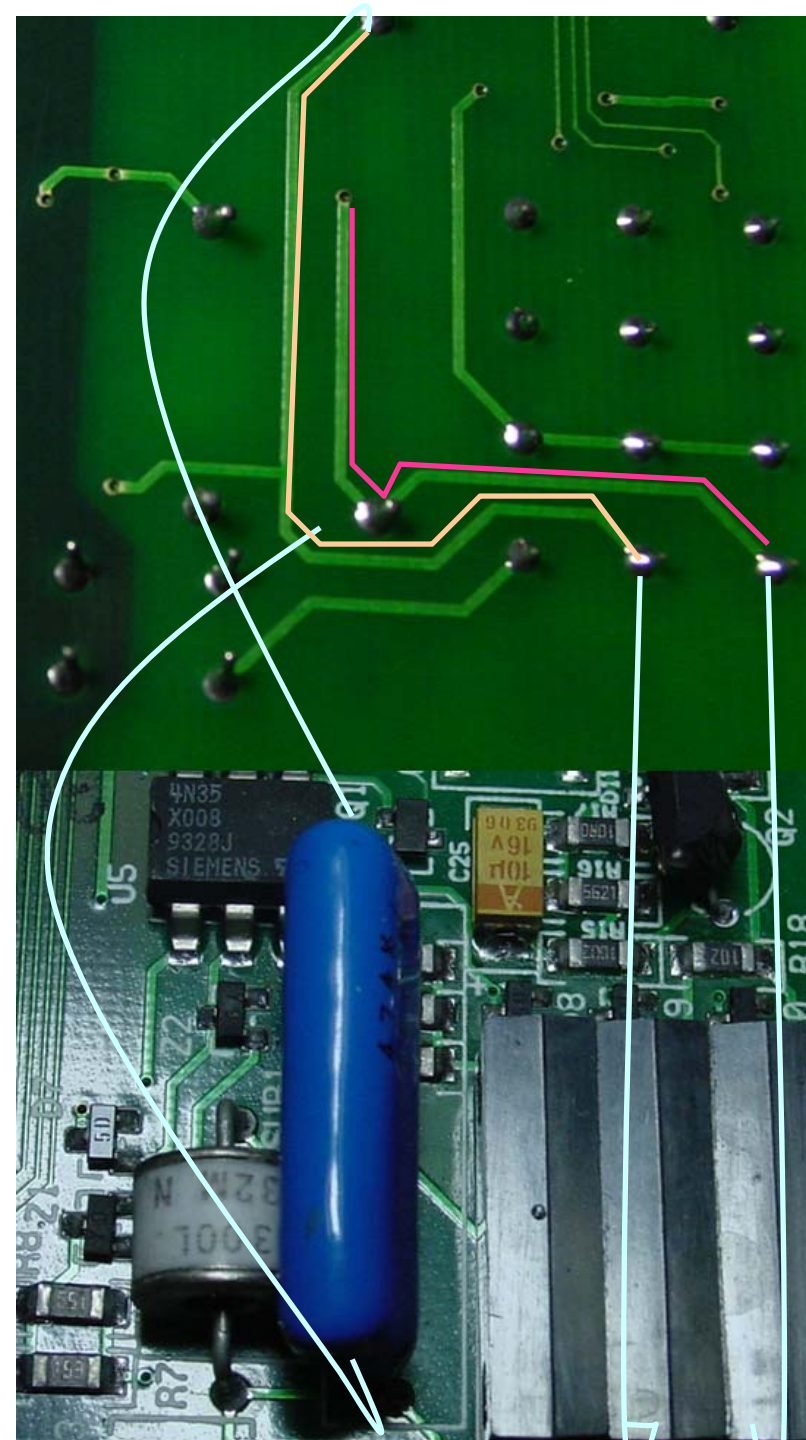
PHYSICAL NODE

PHYSICAL NODE

SECTION OF 14.4 KB VOICE/DATA MODEM

CORRESPONDING POINTS

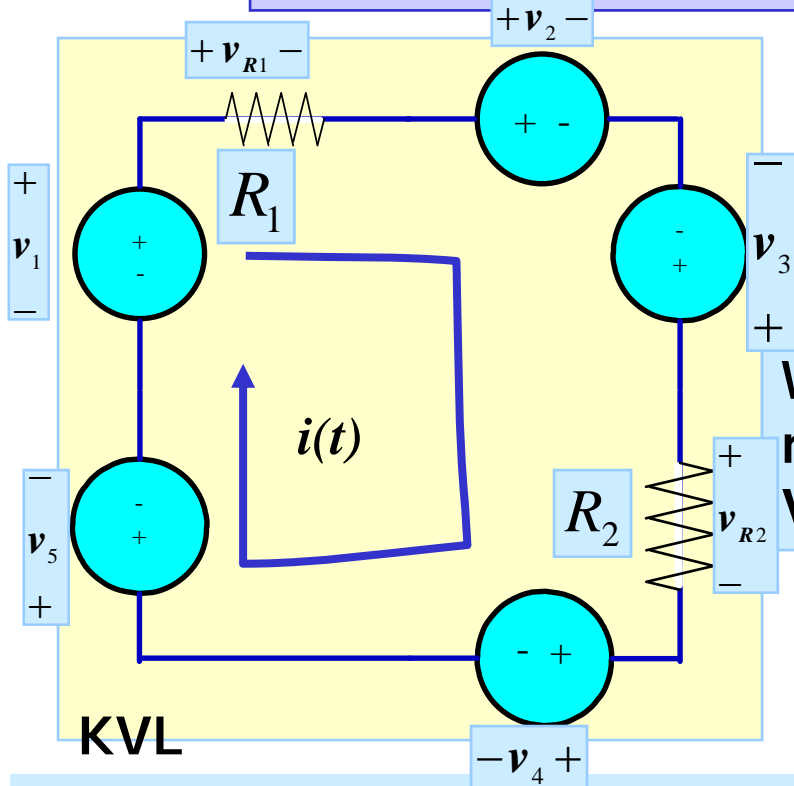
COMPONENT SIDE



FIRST GENERALIZATION: MULTIPLE SOURCES

Voltage sources in series can be algebraically added to form an equivalent source.

We select the reference direction to move along the path.
Voltage drops are subtracted from rises

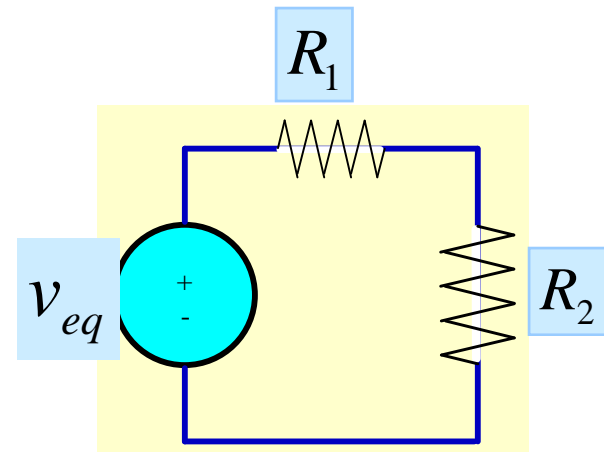


$$v_{R1} + v_2 - v_3 + v_{R2} + v_4 + v_5 - v_1 = 0$$

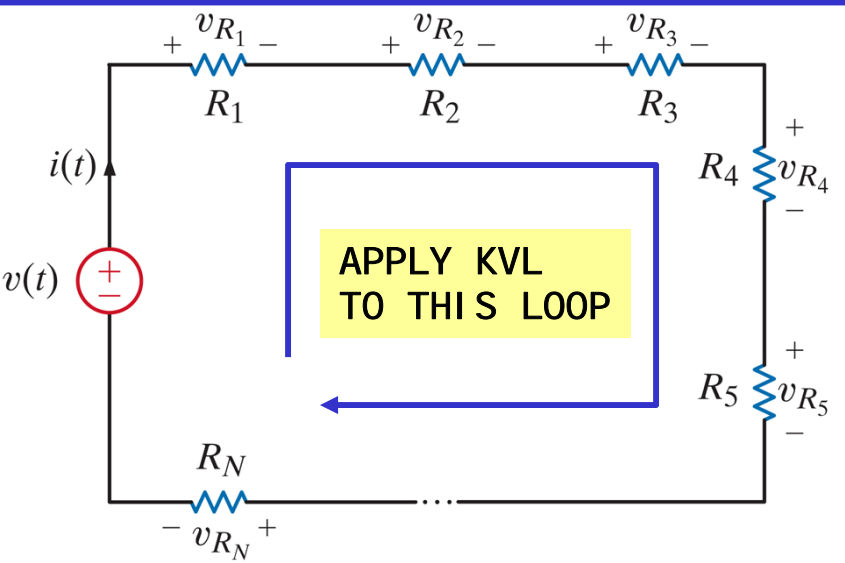
Collect all sources on one side

$$(v_1 - v_2 + v_3 - v_4 - v_5) = v_{R1} + v_{R2}$$

$$(v_{eq}) = v_{R1} + v_{R2}$$



SECOND GENERALIZATION: MULTIPLE RESISTORS



$$v(t) = v_{R_1} + v_{R_2} + \dots + v_{R_N}$$

AND OHM'S LAW
 $= R_1 i(t) + R_2 i(t) + \dots + R_N i(t)$

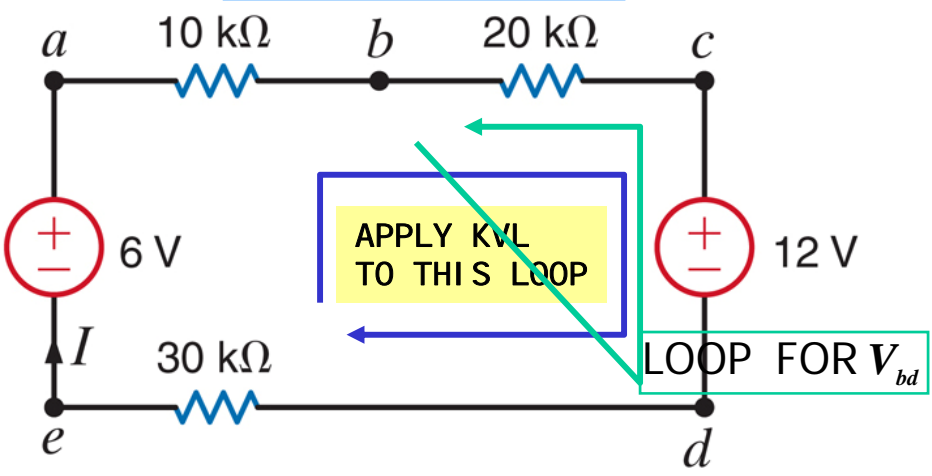
$$R_S = R_1 + R_2 + \dots + R_N$$

$$i(t) = \frac{v(t)}{R_S}$$

$$v_{R_i} = R_i i \Rightarrow v_{R_i} = \frac{R_i}{R_S} v(t)$$

VOLTAGE DIVISION FOR MULTIPLE RESISTORS

FIND $I, V_{bd}, P(30k)$



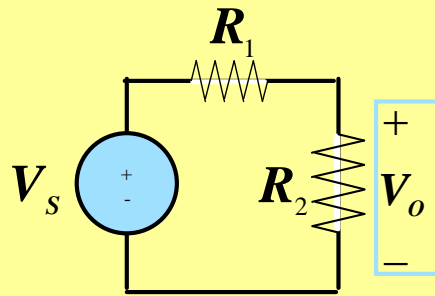
$$10kI + 20kI + 12 + 30kI - 6 = 0$$

$$I = -0.1 \text{ mA}$$

$$V_{bd} - 12 - 20[k\Omega]I = 0 \text{ (KVL)} \Rightarrow V_{bd} = 10V$$

POWER ON $30k\Omega$ RESISTOR
 $P = I^2 R = (-10^{-4} \text{ A})^2 (30 * 10^3 \Omega) = 0.3 \text{ mW}$

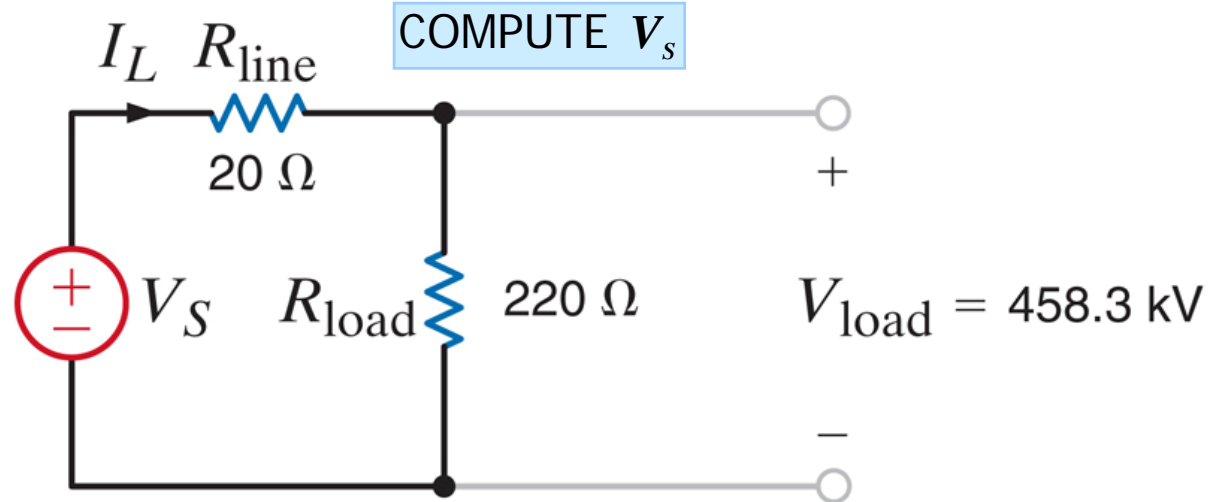
THE "INVERSE" VOLTAGE DIVIDER



VOLTAGE DIVIDER "INVERSE" DIVIDER

$$V_o = \frac{R_2}{R_1 + R_2} V_s$$

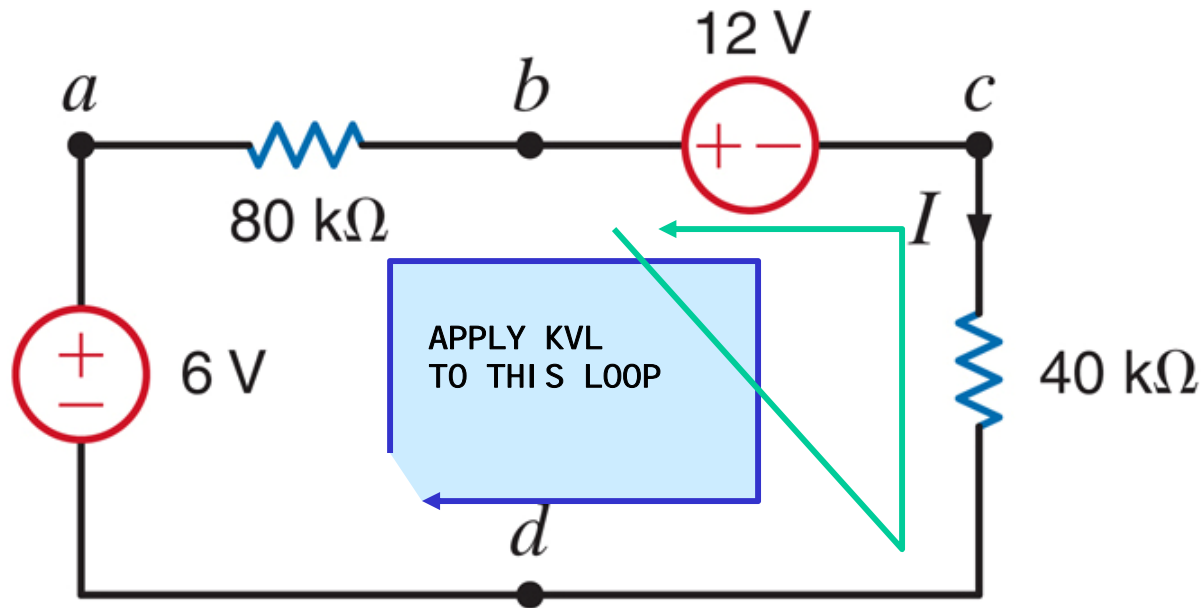
$$V_s = \frac{R_1 + R_2}{R_2} V_o$$



"INVERSE" DIVIDER

$$V_s = \frac{220 + 20}{220} 458.3 = 500 \text{ kV}$$

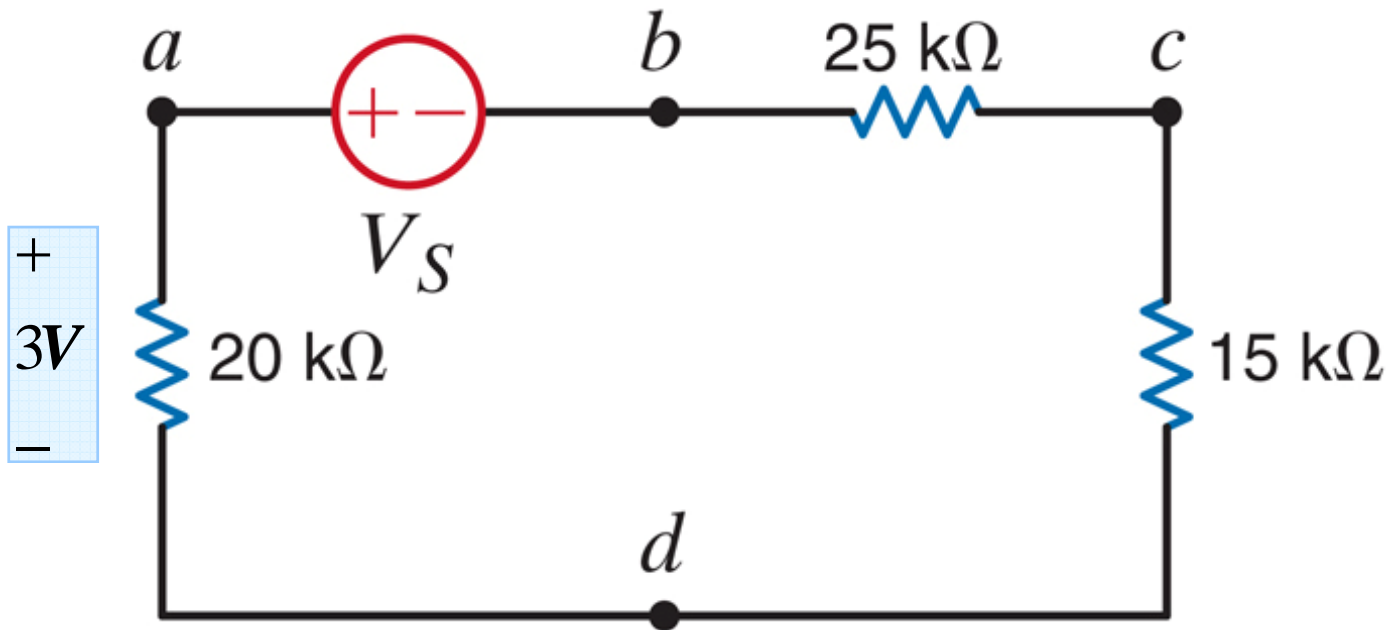
Find I and V_{bd}



$$-6 + 80kI + 12 + 40kI = 0 \Rightarrow I = -0.05\text{mA}$$

$$V_{bd} - 40kI - 12V = 0 \Rightarrow V_{bd} = 10V$$

If $V_{ad} = 3V$, find V_s



INVERSE DIVIDER PROBLEM

$$V_s = \frac{25 + 15 + 20}{20} 3 = 9V$$