CAPACITANCE AND INDUCTANCE

Introduces two passive, energy storing devices: Capacitors and Inductors

CAPACITORS Store energy in their electric field (electrostatic energy) Model as circuit element

INDUCTORS Store energy in their magnetic field Model as circuit element

CAPACITOR AND INDUCTOR COMBINATIONS Series/parallel combinations of elements

CAPACITORS

First of the energy storage devices to be discussed









 ε Dielectric constant of material in gap

PLATE SIZE FOR EQUIVALENT AIR-GAP CAPACITOR

$$55F = \frac{8.85 \times 10^{-12} A}{1.016 \times 10^{-4}} \Longrightarrow A = 6.3141 \times 10^8 m^2$$

Normal values of capacitance are small. Microfarads is common. For integrated circuits nano or pico farads are not unusual



Capacitors only store and release ELECTROSTATIC energy. They do not "create"

The capacitor is a passive element and follows the passive sign convention





Linear capacitor circuit representation

$$i(t) = C \frac{dv}{dt}(t)$$

$$\begin{array}{c} \bullet & \bullet \\ \hline & & i(t) \\ v(t) & & \frown \\ \bullet \\ \bullet \\ & \bullet \\ & (b) \end{array} \end{array} C$$

$$i(t) = -C \frac{dv(t)}{dt}$$

 $Q_C = CV_C$ Capacitance Law

If the voltage varies the charge varies and there is a displacement current

One can also express the voltage across in terms of the current

$$\frac{V_C(t) = \frac{1}{C}Q}{C} = \frac{1}{C} \int_{-\infty}^{t} i_C(x) dx$$

Integral form of Capacitance law

The mathematical implication of the integral form is ...

 $V_C(t-) = V_C(t+); \forall t$

Voltage across a capacitor MUST be continuous ... Or one can express the current through in terms of the voltage across

$$i_{C} = \frac{dQ}{dt} = C \frac{dV_{C}}{dt}$$

Differential form of Capacitance law

Implications of differential form??

 $V_C = Const \Rightarrow i_C = 0$

DC or steady state behavior

A capacitor in steady state acts as an OPEN CIRCUIT



an integral has important implications...







NOTICE USE OF PASSIVE SIGN CONVENTION

Flux lines

Circuit representation for an inductor

Flux lines may extend beyond inductor creating stray inductance effects

A TIME VARYING FLUX CREATES A COUNTER EMF AND CAUSES A VOLTAGE TO APPEAR AT THE TERMINALS OF THE DEVICE

EXAMPLE

FIND THE TOTAL ENERGY STORED IN THE CIRCUIT

In steady state inductors act as short circuits and capacitors act as open circuits

$$W_{c} = \frac{1}{2}CV_{c}^{2}$$
 $W_{L} = \frac{1}{2}LI_{L}^{2}$

$$I_{L1} + 3A = I_{L2} \Rightarrow I_{L1} = -1.2A \qquad V_{C2} = \frac{6}{6+3}V_A = 10.8V$$
$$V_{C1} = 9 - 6I_{L1} \Rightarrow V_{C1} = 16.2V \qquad I_{L2} = \frac{V_A}{9} = 1.8A$$

@
$$A :- 3A + \frac{V_A}{9} + \frac{V_A - 9}{6} = 0$$

 $V_A = \frac{81}{5} [V]$
 $w_{L1} = \frac{1}{2} (2 \times 10^{-3}) (-1.2)^2 = 1.44 \text{ mJ}$

$$w_{L2} = \frac{1}{2} (4 \times 10^{-3})(1.8)^2 = 6.48 \text{ mJ}$$

 $w_{C1} = \frac{1}{2} (20 \times 10^{-6})(16.2)^2 = 2.62 \text{ mJ}$
 $w_{C2} = \frac{1}{2} (50 \times 10^{-6})(10.8)^2 = 2.92 \text{ mJ}$

ENERGY STORED BETWEEN 2 AND 4 ms

$$w(4,2) = \frac{1}{2}Li_L^2(4) - \frac{1}{2}Li_L^2(2)$$

$$w(4,2) = 0 - 0.5 * 10 * 10^{-3} (20 * 10^{-3})^2$$
 J

THE VALUE IS NEGATIVE BECAUSE THE INDUCTOR IS SUPPLYING ENERGY PREVIOUSLY STORED CAPACITOR SPECIFICATIONS

CAPACITANCE RANGE $p F \approx C \approx 50 mF$ IN STANDARD VALUES

STANDARD CAPACITOR RATINGS

6.3V - 500V

STANDARD TOLERANCE

 $\pm 5\%, \pm 10\%, \pm 20\%$

INDUCTOR SPECIFICATIONS

INDUCTANCE RANGES $\approx 1 nH \le L \le 100 mH$ IN STANDARD VALUES STANDARD INDUCTOR RATINGS $\approx mA - \approx 1A$ STANDARD TOLERANCE

 $\pm 5\%, \pm 10\%$

The Dual Relationship for Capacitors and Inductors		
Capacitor		Inductor
$i(t) = C \frac{dv(t)}{dt}$		$v(t) = L \frac{di(t)}{dt}$
$v(t)=\frac{1}{C}\int_{t_0}^t i(x)dx+v(t)$	(t_0)	$i(t) = \frac{1}{L} \int_{t_0}^t v(x) dx + i(t_0)$
$p(t) = Cv(t) \frac{dv(t)}{dt}$	$C \rightarrow L$ $v \rightarrow i$	$p(t) = Li(t)\frac{di(t)}{dt}$
$w(t) = \frac{1}{2}Cv(t)^2$	$i \rightarrow v$	$w(t)=\tfrac{1}{2}Li^2(t)$

IDEAL AND PRACTICAL ELEMENTS

SERIES CAPACITORS

SERIES INDUCTORS

$$i(t) + v_{1}(t) + v_{2}(t) + v_{3}(t) + v_{3}(t) + t_{1} + U_{2} + U_{3}(t) + t_{2} + U_{3}(t) + U_{3}(t) + U_{2} + U_{3}(t) + U_{3}(t$$

SUMMARY

• The important (dual) relationships for capacitors and inductors are as follows:

q = Cv	
$i(t) = C \frac{dv(t)}{dt}$	$v(t) = L \frac{di(t)}{dt}$
$v(t) = \frac{1}{C} \int_{-\infty}^{t} i(x) dx$	$i(t) = \frac{1}{L} \int_{-\infty}^{t} v(x) dx$
$p(t) = Cv(t) \frac{dv(t)}{dt}$	$p(t) = Li(t) \frac{di(t)}{dt}$
$W_C(t) = 1/2Cv^2(t)$	$W_L(t) = 1/2Li^2(t)$

- The passive sign convention is used with capacitors and inductors.
- In dc steady state a capacitor looks like an open circuit and an inductor looks like a short circuit.
- Leakage resistance is present in practical capacitors and inductors.
- When capacitors are interconnected, their equivalent capacitance is determined as follows: Capacitors in series combine like resistors in parallel and capacitors in parallel combine like resistors in series.
- When inductors are interconnected, their equivalent inductance is determined as follows: Inductors in series combine like resistors in series and inductors in parallel combine like resistors in parallel.
- *RC* operational amplifier circuits can be used to differentiate or integrate an electrical signal.

LEARNING EXAMPLE

IC WITH WIREBONDS TO THE OUTSIDE

GOAL: REDUCE INDUCTANCE IN THE WIRING AND REDUCE THE "GROUND BOUNCE" EFFECT

FLIP CHIP MOUNTING

A SIMPLE MODEL CAN BE USED TO DESCRIBE GROUND BOUNCE

IF ALL GATES IN A CHIP ARE CONNECTED TO A SINGLE GROUND THE CURRENT CAN BE QUITE HIGH AND THE BOUNCE MAY BECOME UNACCEPTABLE

USE SEVERAL GROUND CONNECTIONS (BALLS) AND ALLOCATE A FRACTION OF THE GATES TO EACH BALL