

Papers in Refereed Journals

1. Behzad Nouri, E. Gad, Ali Nouri, M. Nakhla, and Y. Tao, "DC-centric parameterized reduced-order model via moment-based interpolation projection (MIP) algorithm", *IEEE Transactions on Components, Packaging and Manufacturing Technology*, vol. 10, no. 8, pp. 1348-1357, August 2020.
2. L. Lombardi, Y. Tao, B. Nouri, F. Ferranti, G. Antonini, and M. Nakhla, "Parameterized model order reduction of delayed PEEC circuits", *IEEE Transactions on Electromagnetic Compatibility*, vol. 62, no. 3, , pp. 859-869, June 2020.
3. L. Lombardi, M. Nakhla, F. Ferranti and G. Antonini, "Time-Domain Sensitivity Analysis of Delayed Partial Element Equivalent Circuits", *IEEE Transactions on Electromagnetic Compatibility*, vol. 61, no. 5, pp. 1465-1473, October 2019.
4. B. Nouri and M. Nakhla, "Efficient time-domain sensitivity analysis of active networks", *IEEE Transactions on Components, Packaging and Manufacturing Technology*, vol. 9, no. 9, pp. 1721-1729, September 2019.
5. M. Plesnik, E. Gad and M. Nakhla, "Efficient steady-state simulation of switching power converter circuits", *IEEE Transactions on Components, Packaging and Manufacturing Technology*, vol. 9, no. 7, pp. 1328-1336, July 2019.
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7. W. Lee, R. Achar and M. Nakhla, "Dynamic GPU parallel sparse LU factorization for fast circuit simulation", *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 26, no. 11, pp. 2518-2529, November 2018.
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