

Switch SPDT , 50Ω

ZYSWA-2-50DR

Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS TTL Low @ 0V (dB) IN-OUT , "ON"	ISOLATION TTL High @ 5V (dB) IN-OUT , "OFF"	VSWR (:1)		
			IN	OUT , "ON"	OUT , "OFF"
3	0.84	65.32	1.16	1.16	1.17
5	0.83	65.59	1.17	1.16	1.17
10	0.83	65.42	1.17	1.16	1.17
20	0.89	66.30	1.17	1.16	1.17
50	0.85	65.63	1.17	1.16	1.18
100	0.87	61.02	1.17	1.16	1.18
200	0.89	53.60	1.18	1.16	1.18
500	0.95	43.89	1.22	1.16	1.20
1000	1.05	37.39	1.28	1.15	1.23
1151	1.05	36.05	1.29	1.15	1.24
1724	1.22	32.34	1.29	1.14	1.26
2000	1.13	30.92	1.27	1.13	1.25
2442	1.17	29.06	1.22	1.15	1.22
2561	1.25	28.66	1.20	1.15	1.22
3135	1.42	26.96	1.11	1.21	1.10
3709	1.48	25.86	1.14	1.27	1.16
4283	1.58	25.02	1.23	1.33	1.24
4713	1.73	24.78	1.21	1.33	1.24
4857	1.80	24.69	1.18	1.34	1.25
5000	1.80	24.46	1.15	1.34	1.26