97.477 Analog Integrated Electronics, Summary

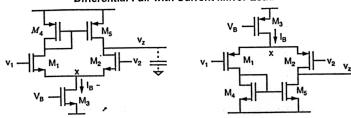
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Differential Pair with Resistors sm. sig: $g_{m1} = g_{m2}$, $r_{eq} = R \parallel r_o$, where $r_o = \frac{1}{g_o}$ of M_2 .

$$v_{o2} = \frac{g_{m1}r_{eq}}{2}(v_1 - v_2), \quad v_{o1} = -\left(\frac{g_{m1}r_{eq}}{2}\right)(v_1 - v_2).$$

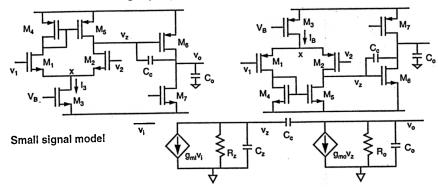
Common-mode gain $v_{o1} = v_{o2} = \frac{g_{o3}}{2} \cdot R \cdot v_C$, where $v_C = \frac{v_1 + v_2}{2}$.

Differential Pair with Current Mirror Load



 t_{r1} is mirrored to M_5 thus doubling output current and gain. difference mode gain $A_d = \frac{v_z}{(v_1 - v_2)} \approx g_m R_{eq}$, or $\frac{g_m}{g_{eq}}$, where $g_{eq} = g_{o5} + g_{o2} = \frac{I_B}{2}(\lambda_2 + \lambda_5)$ common-mode gain $A_C = \frac{v_z}{\left(\frac{v_1 + v_2}{2}\right)} \approx -\frac{g_{o3}}{2(g_{m4} + g_{o4})}$, CMRR = $\frac{A_d}{A_c}$

Two-stage opamp with pole splitting compensationi



Where: $v_l = (v_{ln} - v_{in})$, $g_{mo} = g_{m6}$, $g_{mi} = g_{m1} = g_{m2}$, $R_o = r_{o6} \| r_{o7}$, $C_z = -$ = parasitic cap, $C_o = -$ load cap.

Exact Gain is:
$$\frac{v_o}{v_l} = \frac{g_{m1}}{C_L} \frac{(s - g_{m6}/C_c)}{s^2 + s \left[\frac{g_L}{C_L} + \frac{g_z}{C_c} \frac{(C_L + C_c)}{C_L} + \frac{g_{m6}}{C_L}\right] + \frac{g_z g_L}{C_L C_c}}$$

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$$\text{Low Frequency gain} = \frac{v_o}{v_I} - \frac{v_o}{v_z} \cdot \frac{v_z}{v_I} = A_o = g_{mo}R_o \cdot g_{mI}R_z \qquad \qquad \frac{v_o}{v_I} \sim A_o \frac{\omega_{\rho2}\omega_{\rho2}}{z} \frac{(-s+z)}{(s+\omega_{\rho1})(s+\omega_{\rho2})}$$

dominant pole =
$$\omega_{p1} \approx \frac{1}{R_z g_{mo} R_o C_c}$$
 next pole = $\omega_{p2} \approx \frac{g_{mo}}{C_L}$ RHP zero = $\omega_z \approx \frac{g_{mo}}{C_c}$

stability: p_2 and z must be beyond UGBW enough so total phase shift due to p_2 and z < 45°.

Rules of thumb: Gregorian and Temes $|p_2| = |z| = 3 \text{ UGBW } \rightarrow 53^{\circ} \text{ phase marging}$

Allen and Holberg: |z| = 10 UGBW, $|p_1| = 2.2 \text{ UGBW} \rightarrow 60^{\circ} \text{ phase margin}$

UGBW = $\frac{g_{ml}}{C_c}$, Slew Rate = $\frac{I_3}{C_c}$, Increased load capacitance reduces stability since p_2 moves to lower frequency.

Additions to Pole Splitting: The zero in the RHP can result in instability. Can remove, or compensate for with feedback buffer, or resistor in series with C_{\star} .

Buffer, typically source follower, to allow feedback only (removing feed forward) This removes the zero, and the system is left with two poles. These should be separated by the DC gain, or more for stability.

Series Resistance adds $\omega_{p3} = -\frac{1}{R_c C_c}$, changes ω_z to $\omega_z = \frac{1}{\left(\frac{1}{R_{m6}} - R_c\right) C_c}$, this allows one to move $\omega_z \to \infty$, or bring

it into the LHP where it can cancel out ω_{n2} approximately.

Offsets for opamp gain A: $V_{i, off} = \frac{V_{o, off}}{A}$.

Gain Error with Feedback: For high A, $\frac{v_o}{v_I} = \frac{1/B}{1+1/(AB)} = \frac{1}{B} \left(1 - \frac{1}{AB}\right)$, here, desired gain $= \frac{1}{B}$, Gain error $= \frac{1}{AB}$ where $AB = \text{loop gain} \rightarrow \text{Open loop gain In dB}$, Closed loop gain is $\frac{1}{B}$ in dB

Folded Cascode Amplifier

- single stage (output is directly $\frac{g_{ml}}{2}v_{ln}$) high frequency capability - gain = $\frac{v_o^+ - v_o^-}{v_{ln}^+ - v_{ln}^-} = g_{ml}R_o$, typical gain about 1000 or 60 dB. R_o is high due to cascodes- Dominant pole, UGBW is set by load capacitance thus larger load results in more stability